

**SEARCH RESULTS**

You searched for: (cadence prediction)

Results per Page  Showing 1 - 20 of 20 results

**Wearable sensor systems in biomechanics:  
assessment of unrestrained walking features**

Sabatini, A.M.;  
Instrumentation and Measurement Technology Conference,  
2004. IMTC 04. Proceedings of the 21st IEEE  
Volume: 2  
Digital Object Identifier: 10.1109/IMTC.2004.1351203  
Publication Year: 2004 , Page(s): 881 - 883 Vol.2

**IEEE CONFERENCES****Evaluation of Hot-Electron Effect on LDMOS Device  
and Circuit Performances**

Jiann-Shiun Yuan; Jiang, L.;  
Electron Devices, IEEE Transactions on  
Volume: 55 , Issue: 6  
Digital Object Identifier: 10.1109/TED.2008.922850  
Publication Year: 2008 , Page(s): 1519 - 1523  
Cited by: 2

**IEEE JOURNALS****Efficient critical area estimation for arbitrary defect  
shapes**

Allan, G.A.; Walton, A.J.;  
Defect and Fault Tolerance in VLSI Systems, 1997.  
Proceedings., 1997 IEEE International Symposium on  
Digital Object Identifier: 10.1109/DFTVS.1997.628305  
Publication Year: 1997 , Page(s): 20 - 28  
Cited by: 2

**IEEE CONFERENCES****Improving the efficacy of electrical stimulation-  
induced leg cycle ergometry: an analysis based on a  
dynamic musculoskeletal model**

Schutte, L.M.; Rodgers, M.M.; Zajac, F.E.; Glaser, R.M.;  
Rehabilitation Engineering, IEEE Transactions on  
Volume: 1 , Issue: 2  
Digital Object Identifier: 10.1109/86.242425  
Publication Year: 1993 , Page(s): 109 - 125  
Cited by: 13

## IEEE JOURNALS

**Electrical and Temperature Stress Effects on Class-AB Power Amplifier Performances**

Chuanzhao Yu; Yuan, J.S.;

Electron Devices, IEEE Transactions on

Volume: 54 , Issue: 6

Digital Object Identifier: 10.1109/TED.2007.896601

Publication Year: 2007 , Page(s): 1346 - 1350

## IEEE JOURNALS

**Linearity analysis and design optimisation for 0.18  $\mu\text{m}$  CMOS RF mixer**

Li, Q.; Yuan, J.S.;

Circuits, Devices and Systems, IEE Proceedings -

Volume: 149 , Issue: 2

Digital Object Identifier: 10.1049/ip-cds:20020355

Publication Year: 2002 , Page(s): 112 - 118

Cited by: 10

## IET JOURNALS

**Analytical input mapping for modelling energy dissipation of complex CMOS gates**

Di, J.; Yuan, J.S.; Hagedorn, M.;

Circuits, Devices and Systems, IEE Proceedings -

Volume: 151 , Issue: 4

Digital Object Identifier: 10.1049/ip-cds:20040160(410) 151

Publication Year: 2004 , Page(s): 294 - 299

## IET JOURNALS

**CMOS RF mixer no-linearity design**

Qiang Li; Jinlong Zhang; Wei Li; Yuan, J.S.;

Circuits and Systems, 2001. MWSCAS 2001. Proceedings of the 44th IEEE 2001 Midwest Symposium on

Volume: 2

Digital Object Identifier: 10.1109/MWSCAS.2001.986310

Publication Year: 2001 , Page(s): 808 - 811 vol.2

Cited by: 2

## IEEE CONFERENCES

**Observing Walking Behavior of Humans Using Distributed Phenomenon Detection and Tracking Mechanisms**

Bose, R.; Helal, A.;

Applications and the Internet, 2008. SAINT 2008. International Symposium on

Digital Object Identifier: 10.1109/SAINT.2008.88

Publication Year: 2008 , Page(s): 405 - 408

## IEEE CONFERENCES

---

**Natural gait parameters prediction for gait rehabilitation via artificial neural network**

Lim, H.B.; Trieu Phat Luu; Hoon, K.H.; Low, K.H.;  
Intelligent Robots and Systems (IROS), 2010 IEEE/RSJ  
International Conference on  
Digital Object Identifier: 10.1109/IROS.2010.5650311  
Publication Year: 2010 , Page(s): 5398 - 5403

IEEE CONFERENCES

---

**Memory performance prediction for high-performance microprocessors at deep submicrometer technologies**

Zeng, A.; Rose, K.; Guimann, R.J.;  
Computer-Aided Design of Integrated Circuits and Systems,  
IEEE Transactions on  
Volume: 25 , Issue: 9  
Digital Object Identifier: 10.1109/TCAD.2005.858346  
Publication Year: 2006 , Page(s): 1705 - 1718

IEEE JOURNALS

---

**Accurate electrical battery model capable of predicting runtime and I-V performance**

Min Chen; Rincon-Mora, G.A.;  
Energy Conversion, IEEE Transactions on  
Volume: 21 , Issue: 2  
Digital Object Identifier: 10.1109/TEC.2006.874229  
Publication Year: 2006 , Page(s): 504 - 511  
Cited by: 53

IEEE JOURNALS

---

**Concurrent transient fault simulation for analog circuits**

Junwei Hou; Chatterjee, A.;  
Computer-Aided Design of Integrated Circuits and Systems,  
IEEE Transactions on  
Volume: 22 , Issue: 10  
Digital Object Identifier: 10.1109/TCAD.2003.818129  
Publication Year: 2003 , Page(s): 1385 - 1398  
Cited by: 2

IEEE JOURNALS

---

**Timing and signal integrity analysis in a cell based SOI design**

Griyage, D.K.; Lokanandham, S.; Jacobs, M.;  
SOI Conference, 2009 IEEE International  
Digital Object Identifier: 10.1109/SOI.2009.5318731  
Publication Year: 2009 , Page(s): 1 - 4

IEEE CONFERENCES

---

**A low complexity carrier recovery and frequency estimation algorithm for Iridium handset system**

Syang-Myau Hwang; Mao Yu; Lin Yang;  
Wireless Communications and Networking Conference, 1999.  
WCNC. 1999 IEEE  
Digital Object Identifier: 10.1109/WCNC.1999.796927  
Publication Year: 1999 , Page(s): 1208 - 1211 vol.3

**IEEE CONFERENCES**

---

**Combined topological and functionality-based delay estimation using a layout-driven approach for high-level applications**

Ramachandran, C.; Kurdahi, F.J.;  
Computer-Aided Design of Integrated Circuits and Systems,  
IEEE Transactions on  
Volume: 13 , Issue: 12  
Digital Object Identifier: 10.1109/43.331402  
Publication Year: 1994 , Page(s): 1450 - 1460  
Cited by: 3

**IEEE JOURNALS**

---

**Design and analysis of a regulated peak-power tracking system**

Huynh, P.T.; Cho, B.H.;  
Aerospace and Electronic Systems, IEEE Transactions on  
Volume: 35 , Issue: 1  
Digital Object Identifier: 10.1109/7.745682  
Publication Year: 1999 , Page(s): 84 - 92  
Cited by: 1

**IEEE JOURNALS**

---

**Accurate computation of field reject ratio based on fault latency**

Das, D.; Seth, S.C.; Agrawal, V.D.;  
Very Large Scale Integration (VLSI) Systems, IEEE  
Transactions on  
Volume: 1 , Issue: 4  
Digital Object Identifier: 10.1109/92.250201  
Publication Year: 1993 , Page(s): 537 - 545

**IEEE JOURNALS**

---

**NQS effects on MOSFET's terminal currents**

Yutao Ma; Hancheng Liang; Min-Chie Jeng; Zhihong Liu;  
Solid-State and Integrated Circuit Technology, 2006. ICSICT  
'06. 8th International Conference on  
Digital Object Identifier: 10.1109/ICSICT.2006.306112  
Publication Year: 2006 , Page(s): 1275 - 1277

**IEEE CONFERENCES**

---

**What is the appropriate model for crosstalk control?**

Scheffer, L.;

Integrated Circuits and Systems Design, 2000. Proceedings.

13th Symposium on

Digital Object Identifier: 10.1109/SBCCI.2000.876048

Publication Year: 2000 , Page(s): 315 - 320

Cited by: 1

IEEE CONFERENCES